THESIS SUPERVISOR'S REPORT

I. IDENTIFICATION DATA

Thesis title:	CAN FD Gateway
Author's name:	Srinath Rangarajan
Type of thesis :	master
Faculty/Institute:	Faculty of Electrical Engineering (FEE)
Department:	Dept. of Measurement.
Thesis reviewer:	Assoc. prof. Jiří Novák, Ph.D.
Reviewer's department:	Dept. of Measurement

II. EVALUATION OF INDIVIDUAL CRITERIA

Assignment

How demanding was the assigned project?

Student has focused on two main areas – the embedded hardware design and embedded software development. In both of them the assignment was ordinarily challenging. As the thesis submission deadline was postponed by one semester, the assignment was slightly modified in order to control the Gateway functionality over the CAN interface instead of RS232.

Fulfilment of assignment

How well does the thesis fulfil the assigned task? Have the primary goals been achieved? Which assigned tasks have been incompletely covered, and which parts of the thesis are overextended? Justify your answer. All thesis topics were successfully finished.

Activity and independence when creating final thesis

Assess whether the student had a positive approach, whether the time limits were met, whether the conception was regularly consulted and whether the student was well prepared for the consultations. Assess the student's ability to work independently.

The theses development process was affected by corona epidemic and our personal contact was limited. In some phases we were not in touch for several months - I would prefer more frequent consulting. This is especially true for the time period before the final deadline. On the other hand I have to confirm that student was able to work without my direct support.

Technical level

Is the thesis technically sound? How well did the student employ expertise in his/her field of study? Does the student explain clearly what he/she has done?

The level of the final implementation is very good – both the hardware and software design are correct and Gateway is working. On the other hand the way the design and development process in thesis in only sufficient, many ideas are not described in detail.

Formal level and language level, scope of thesis

Are formalisms and notations used properly? Is the thesis organized in a logical way? Is the thesis sufficiently extensive? Is the thesis well-presented? Is the language clear and understandable? Is the English satisfactory?

The thesis is structured in a standard way and covers (with the reservations mentioned above) all the topics. Thesis is written in good English with minor shortcomings.

Selection of sources, citation correctness

Does the thesis make adequate reference to earlier work on the topic? Was the selection of sources adequate? Is the student's original work clearly distinguished from earlier work in the field? Do the bibliographic citations meet the standards?

Some figures in theses are not cited (even though they are taken from the cited literature). The form of the references is a bit strange.

fulfilled

C - good.

ordinarily challenging

C - good.

D - satisfactory.

C - good.



THESIS SUPERVISOR'S REPORT



Additional commentary and evaluation (optional)

Comment on the overall quality of the thesis, its novelty and its impact on the field, its strengths and weaknesses, the utility of the solution that is presented, the theoretical/formal level, the student's skillfulness, etc.

III. OVERALL EVALUATION, QUESTIONS FOR THE PRESENTATION AND DEFENSE OF THE THESIS, SUGGESTED GRADE

Summarize your opinion on the thesis and explain your final grading.

Finally I would like to declare that student has fulfilled all thesis assignment topics. Implemented CAN FD Gateway module is fully working and will be used in further project focused on test automation in automotive industry. With respect to the issues mentioned above

The grade that I award for the thesis is C - good.

Date: 20.1.2021

Signature: