# Supervisor's statement of a final thesis

### Czech Technical University in Prague

#### Faculty of Information Technology

Student:	Denis Titov
Supervisor:	Ing. Stanislav Jeřábek
Thesis title:	Improvement of Contactless Smart Card Emulator in FPGA
Branch of the study:	Information Technology

#### Date: 12. 6. 2017

Evaluation criterion:	The evaluation scale: 1 to 5.
<ol> <li>Difficulty and other comments on the assignment</li> </ol>	<ol> <li>1 = extremely challenging assignment,</li> <li>2 = rather difficult assignment,</li> <li>3 = assignment of average difficulty,</li> <li>4 = easier, but still sufficient assignment,</li> <li>5 = insufficient assignment</li> </ol>
	s. Comment what is difficult about this thesis (in case of a more difficult thesis, you may n the contrary, with an easy assignment those shortcomings should be evaluated more
Comments:	
The taks of the thesis contained ISO standard studying, existing extensions/enhancements, designing new proprietary protoco protocols between parts of the system. Finally it involved testin Because of the thesis requires to study standard, understand e as more difficult.	I for data processing and also several communication ng of the whole enhancement system.
Evaluation criterion:	The evaluation scale: 1 to 4.
2. Fulfilment of the assignment	1 = assignment fulfilled, 2 = assignment fulfilled with minor objections, <b>3 = assignment fulfilled with major objections,</b> 4 = assignment not fulfilled
	s of the assignment that have not been fulfilled, completely or partially, or extensions of , try to assess the importance, impact, and possibly also the reason of the insufficiencies.
Comments:	
Analysis and implementaion part is really good. There are mad	e several well analysed decisions and implementaion seems to
be logical correct.	
However there is almost completely missing testing because of	
test.	). There is no test of the whole system at once and no practical
Evaluation criterion:	The evaluation scale: 1 to 4.
3. Size of the main written part	1 = meets the criteria, <b><u>2</u> = meets the criteria with minor objections,</b> 3 = meets the criteria with major objections, 4 = does not meet the criteria
Criteria description: Evaluate the adequacy of the extent of the final thesis, considering its content and the si does not contain unnecessary parts.	ize of the written part, i.e. that all parts of the thesis are rich on information and the text
Comments:	
Except for testing (which was only minimal) thesis fulfills all rec	
Evaluation criterion:	The evaluation scale: 0 to 100 points (grade A to F).
4. Factual and logical level of the thesis	95 (A)
Criteria description: Assess whether the thesis is correct as to the facts or if there are factual errors and inacc the comprehensibility of the text for a reader.	uracies. Evaluate further the logical structure of the thesis, links among the chapters, and
Comments:	
Thesis is logically structured, there are made several well analy correct. Thesis contains description of ISO 14443-4 standard ar context of FPGA programming.	nd also discuss several requirements from the standard in
I would just appreciate a little more figures or tables of data flo flows would be easier to understand with them.	ow examples. Text description of the protocol and the data

Evaluation criterion:

## 5. Formal level of the thesis

Criteria description

Assess the correctness of formalisms used in the thesis, the typographical and linguistic aspect s, see Dean's Directive No. 14/2015, Article 3.

Comments: The text contains some typographic mistakes, however, the understanding is not affected at all. The thesis is relatively easily readable and, as far as I can grade it, its English language level is average or better. The evaluation scale: 0 to 100 points (grade A to F). Evaluation criterion. 90 (A) Bibliography 6. Criteria description: Evaluate the student's activity in acquisition and use of studying materials in his thesis. Characterize the choice of the sources. Discuss whether the student used all relevant sources, or whether he tried to solve problems that were already solved. Verify that all elements taken from other sources are properly differentiated from his own results and contributions. Comment if there was a possible violation of the citation ethics and if the bibliographical references are complete and in compliance with citation standards. Comments: Although there are only a few references it responds to very practical focus of the thesis. The main references are existing work and ISO standards, which requirements are implemented into existing work or the brand new parts of the emulator system. I appreciate using of libraries for implementing communication between individual parts of the system Evaluation criterion: The evaluation scale: 0 to 100 points (grade A to F). 7. Evaluation of results. 85 (B) publication outputs and awards Criteria description: Comment on the achieved level of major results of the thesis and indicate whether the main results of the thesis extend published state-of-the-art results and/or bring completely new findings. Assess the quality and functionality of hardware or software solutions. Alternatively, evaluate whether the software or source code that was not created by the student himself was used in accordance with the license terms and copyright. Comment on possible publication output or awards related to the thesis. Comments: Student implemented required extension for run-time configuration and also all mandatory parts of standard 14443-4. Only simple echo has been implemented as proprietary protocol but implementation of some more complicated protocol was not goal of the thesis. There are missing some more complex and deep tests of the system. We just have to believe that all parts of the system tested in simulation will work correctly individually and also as parts of the whole system. Some partially results of this thesis have been used in paper on conference MECO 2017. No evaluation scale Evaluation criterion. Applicability of the results Criteria description Indicate the potential of using the results of the thesis in practice. Comments: Results could be used for demonstration and study purposes. It should be deeply tested and after that could the work extended with optionall commands support or with another HW resources (microcontroller). In the thesis is dominating implementation of ISO standard but there are also some discussion about its difficulties when implementing on FPGA. Evaluation criterion: The evaluation scale: 1 to 5. 9. Activity and self-reliance of the 9a: 1 = excellent activity, student 2 = very good activity, 3 = average activity, 4 = weaker, but still sufficient activity, 5 = insufficient activity 9h: 1 = excellent self-reliance, 2 = very good self-reliance, 3 = average self-reliance, 4 = weaker, but still sufficient self-reliance, 5 = insufficient self-reliance. Criteria description Review student's activity while working on this final thesis, student's punctuality when meeting the deadlines and consulting continuously and also, student's preparedness for these consultations. Furthermore, review student's independency. Comments: Student was working very hard but unfortunately mainly in a few last month. He worked very independently but he had to discuss some technical and ISO standard details to understand them properly. Evaluation criterion: The evaluation scale: 0 to 100 points (grade A to F). 10. The overall evaluation 83 (B) Criteria description Summarize the parts of the thesis that had major impact on your evaluation. The overall evaluation does not have to be the arithmetic mean or any other formula with the values from the previous evaluation criteria 1 to 9 Comments: All mandatory parts of the thesis assignemt were studied, analysed and implemented. Results of the work are really promising, the analysis, decisions and implemetation seems to be logical and well-done. However it is risky to mark the final solution as working with only individually behavioral (unit) tests of parts of the system with grey-box testing of typical situations approach.