



## ZÁPIS O PRŮBĚHU OBHAJOBY

Titul, jméno, příjmení : **Ing. Emil Eyvazov**  
Studijní program : **Informatics**  
Studijní specializace : **Design and Programming of Embedded Systems 2021**  
Téma diplomové práce : **Vectorová akcelerační jednotka pro FPGA**  
Diploma Thesis : **Vector Processing Unit on FPGA**  
Vedoucí práce : **Ing. Miroslav Skrbek, Ph.D.**  
Oponent práce : **doc. Ing. Jan Schmidt, Ph.D.**  
Konané dne : **11.02.2026**  
Hodnocení práce : **Dobře (C)**

### Průběh obhajoby :

In presentation, the student acquainted the committee and all present with the topic of the final thesis, the process of its solution and the results achieved. This was followed by reading the evaluation of the supervisor and the report of the reviewer of the thesis. Subsequently, there was a discussion of the thesis, including answers to questions from the review.

The proposed evaluation of the supervisor was C, the proposed evaluation of the reviewer was C.

The committee considered both proposals, took into account the presentation of the thesis by the student and determined the final grade.

Návrh hodnocení: **Ing. Miroslav Skrbek, Ph.D. - C**  
**doc. Ing. Jan Schmidt, Ph.D. - C**

Složení komise : **prof. Ing. Hana Kubátová, CSc. (předseda)**  
**Ing. Miroslav Skrbek, Ph.D. (místopředseda)**  
**doc. Ing. Petr Fišer, Ph.D.**  
**Dr. techn. Ing. Jan Legerský**  
**doc. Ing. Ivan Šimeček, Ph.D.**