THESIS REVIEWER'S REPORT



I. IDENTIFICATION DATA

Thesis title: Author's name:	Atomic Clock Backup Power System David Šibrava	
Type of thesis :		
Faculty/Institute:		
Department:	Department of Measurement	
Thesis reviewer:	Alexander Kuna, Ph.D.	
Reviewer's department:	Institute of Photonics and Electronics, Academy of Sciences	

II. EVALUATION OF INDIVIDUAL CRITERIA

Assignment

How demanding was the assigned project?

The project was assigned with dedicated HW and SW design and implementation, microcontroller programming including few peripherals.

Fulfilment of assignment

How well does the thesis fulfil the assigned task? Have the primary goals been achieved? Which assigned tasks have been incompletely covered, and which parts of the thesis are overextended? Justify your answer.

All the assigned tasks were fulfilled as required. I appreciate the variety of the disciplines needed to reach the goal of the project.

Methodology

Comment on the correctness of the approach and/or the solution methods. I find all the methods used in the project correct.

Technical level

Is the thesis technically sound? How well did the student employ expertise in the field of his/her field of study? Does the student explain clearly what he/she has done?

The thesis is at the very good technical level, it is clearly explained what has been done in the project with one small exception.

Formal and language level, scope of thesis

Are formalisms and notations used properly? Is the thesis organized in a logical way? Is the thesis sufficiently extensive? Is the thesis well-presented? Is the language clear and understandable? Is the English satisfactory? The thesis is formally on a very good level. I find the language understandable and satisfactory.

Selection of sources, citation correctness

Does the thesis make adequate reference to earlier work on the topic? Was the selection of sources adequate? Is the student's original work clearly distinguished from earlier work in the field? Do the bibliographic citations meet the standards?

The thesis contains references to the earlier work. The list of references consists of 30 items in total covering data sheets as well as technical notes, manuals etc.

Additional commentary and evaluation (optional)

None.

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III. OVERALL EVALUATION, QUESTIONS FOR THE PRESENTATION AND DEFENSE OF THE THESIS, SUGGESTED GRADE

I appreciate this kind of multi-disciplinary project with obvious practical usability. The student proved good knowledge in hardware design, programming the micro-controller and its peripherals (GSM module, display). I would like to ask one question: The figure 2.1 does not correspond with the preceding and the following text regarding the DC power range of the cesium clock, the number of 12-V batteries, and how these are connected - in series or in parallel? A series connection of the batteries is mentioned, but the parallel connection can be seen in the picture.

The grade that I award for the thesis is

Date: January 31, 2020

Signature: