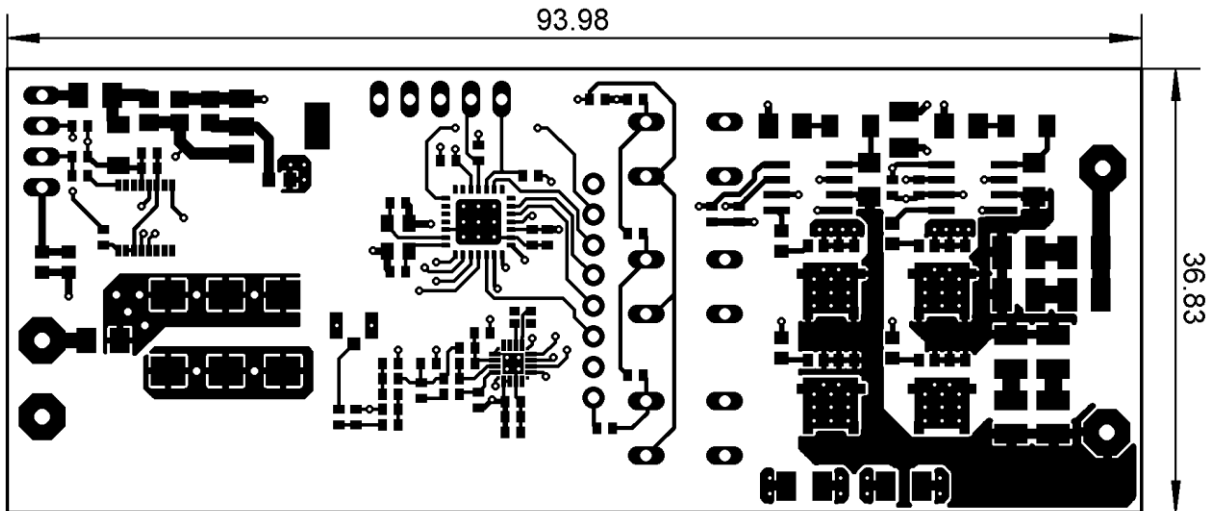


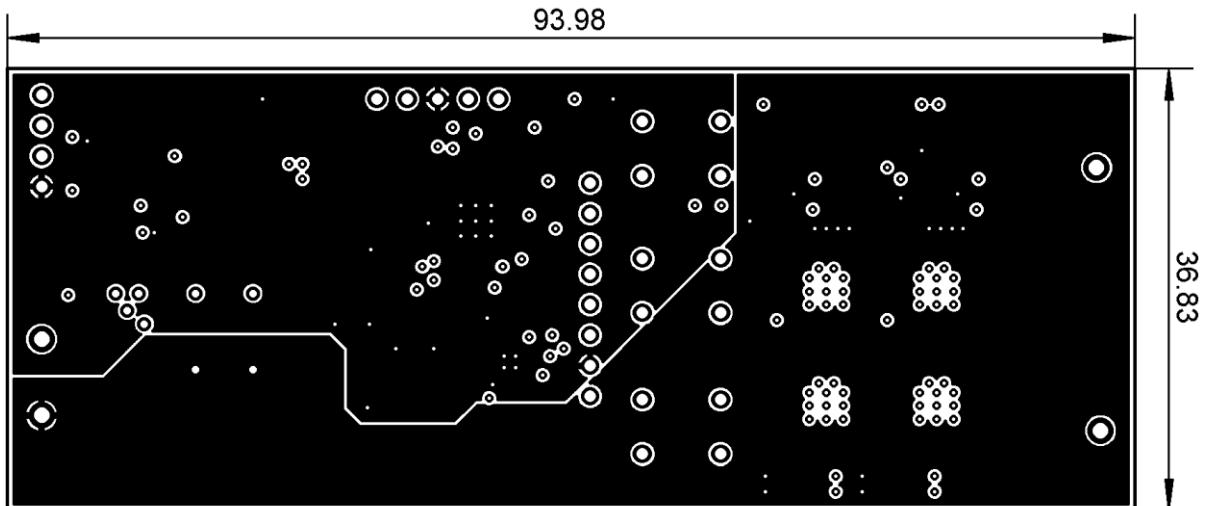
# Appendix 1 – Board layouts

## Charger device

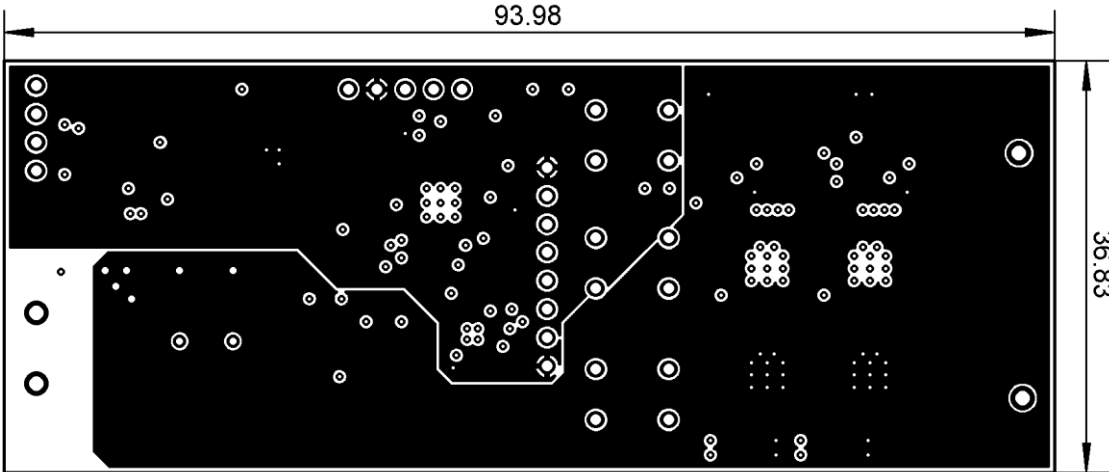
### Top layer



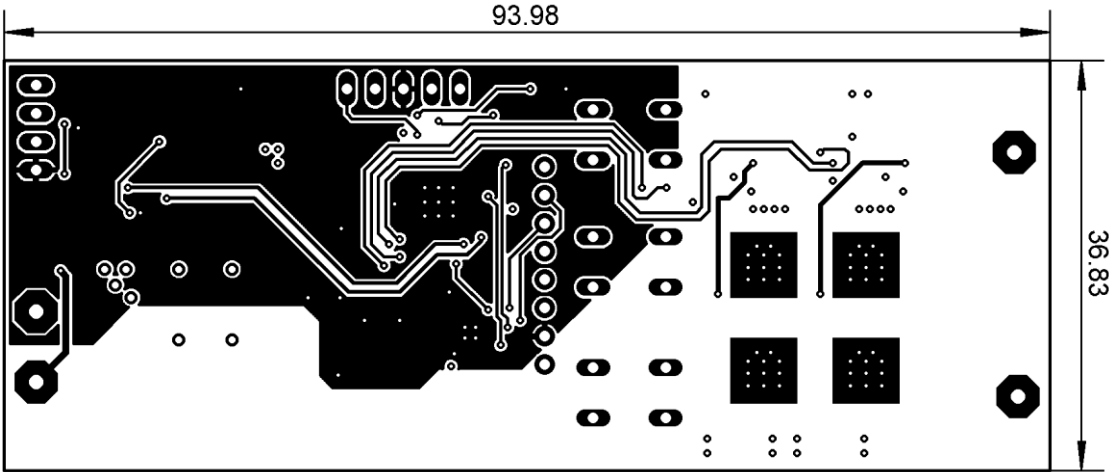
### First inner layer



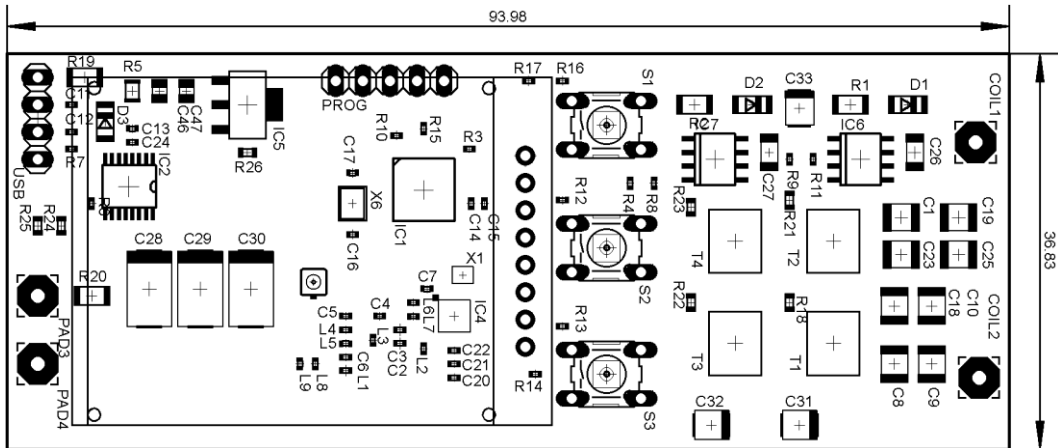
**Second inner layer**



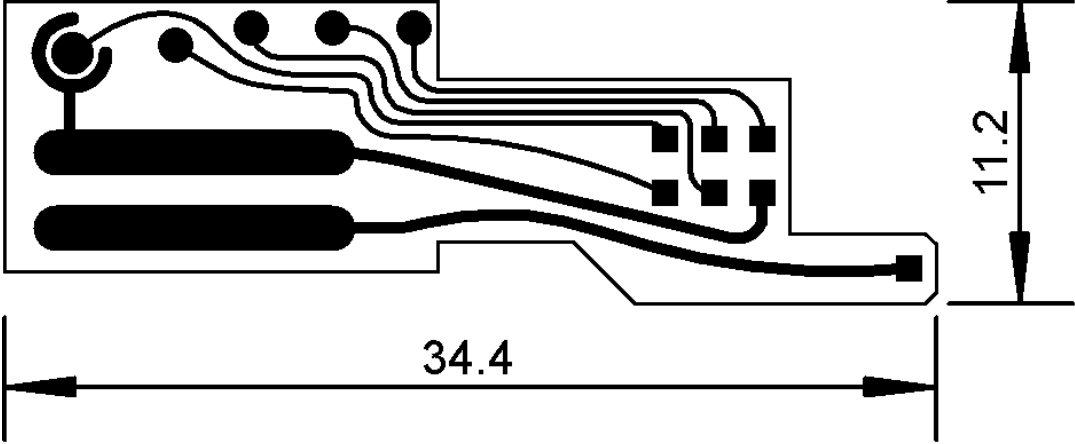
**Bottom layer**



# Component placement

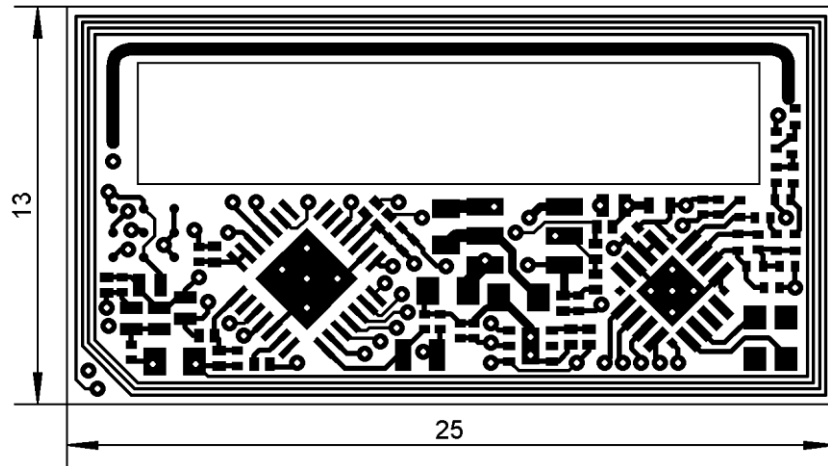


**Flex electrode**

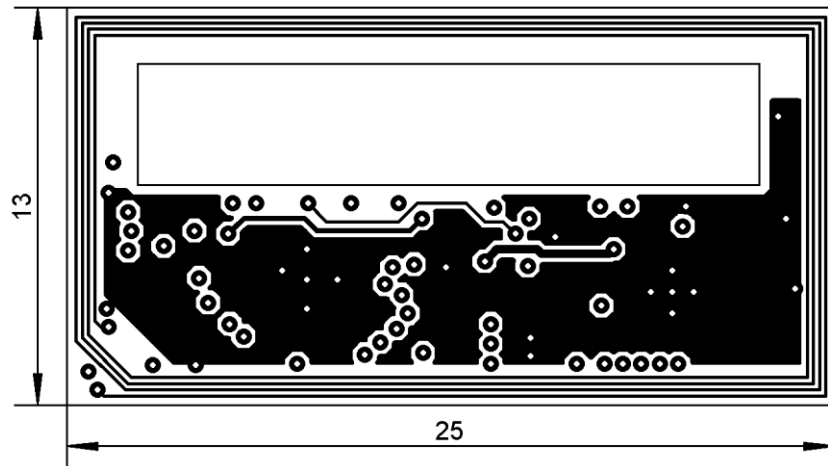


# Implantable device

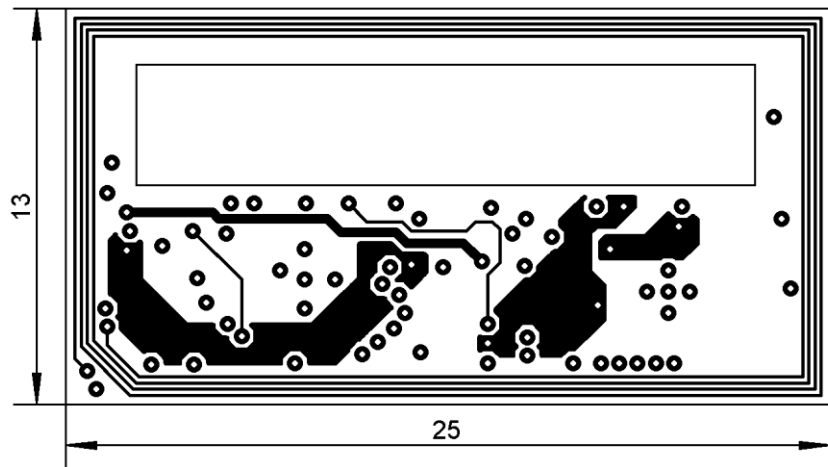
## Top layer



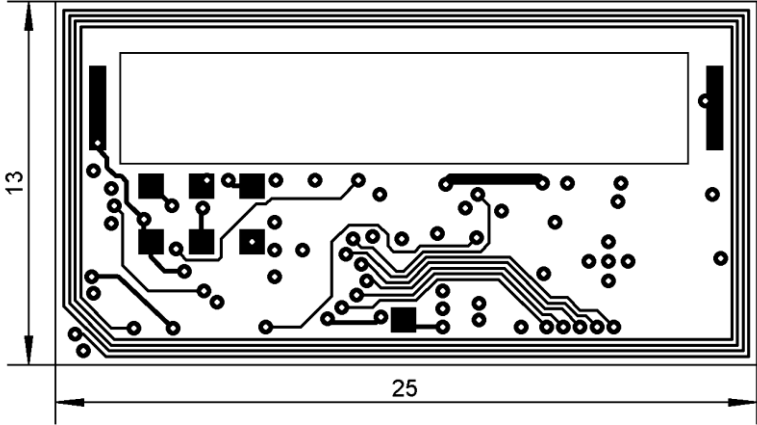
## First inner layer



## Second inner layer



**Bottom layer**



**Component placement**

