Quasi-Elliptic Microstrip Low-Pass Filters Using an Interdigital DGS Slot
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Abstract—This letter introduces a DGS slot with an interdigital shape. The resonant frequency of the slot can be easily controlled by changing the length of the metal fingers, without changing the area taken by the structure. Using this slot, two quasi-elliptic low-pass filters were designed, fabricated and tested. The filters have a cut-off frequency of about 3 GHz.

Index Terms—Defected ground structure (DGS), filter, low-pass filters, microstrip filter, transmission zero.

I. INTRODUCTION

Defected ground structures (DGS) for microstrip lines have been attracting researchers in recent years. They have been presented in a number of different shapes for filter applications [1], [2]. This technique is suitable for periodic structures, and for both low-pass and band-pass filters, e.g., [3]–[8]. The DGS applied to a microstrip line causes a resonant character of the structure transmission with a resonant frequency controllable by changing the shape and size of the slot.

This letter introduces a DGS in the form of an interdigital slot. The resonant frequency of the structure with this slot can be controlled by adjusting the distance between the metal fingers without changing the area occupied by the slot or the aperture. Two quasi-elliptic low-pass filters based on this slot were designed and fabricated on an RO4003c substrate 0.831 mm in thickness and with a relative dielectric constant of 3.38. The resonant behavior of the interdigital DGS used here introduces transmission zeroes to the filter response and consequently improves its stop-band performance.

II. INTERDIGITAL DGS

The proposed interdigital DGS slot is shown in Fig. 1. All dimensions in this letter are in mm. The slot is etched in the ground metallization under the microstrip line. This slot has a major advantage in providing tighter capacitive coupling to the line in comparison to known microstrip DGS structures. Moreover, the resonant frequency of the structure can be controlled by changing the distance between the metal fingers. The resonant frequency of the slot can also be modified by changing the number of metal fingers, so there is in most cases no need to enlarge the overall slot size.

The equivalent circuit of the proposed structure, illustrated in Fig. 2, is a combination of the interdigital capacitance equivalent circuit [9] and the equivalent circuit of the DGS [1], [2]. The series circuit $C_{L_{res}}$ represents an equivalent circuit of the interdigital capacitance [9].

A parametric study was carried out to show the effect of the finger length on the resonant frequency, particularly on the resonance of the parallel resonator shown in the equivalent circuit in Fig. 2, which represents a transmission zero. This study was done by placing a slot under a transmission line 0.2 mm wide. The total width of the slot was fixed at 4.9 mm, and the two different slot lengths $S = 3.75$ and $5.65$ mm were used. The number of metal fingers was fixed at 6. The width and spacing of the fingers are equal to 0.3 mm, as shown in Fig. 1. Fig. 3 shows the dependence of the resonant frequency on the length of the metal fingers $l$. By increasing $l$, capacitance $C$ is raised, and therefore the resonant frequency of the slot, the transmission zero, is shifted down. Capacitance $C$ can be as well changed by changing the spacing between the metal fingers. The narrower this spacing is, the higher the capacitance, and the resonant frequency goes down.

III. LOW-PASS FILTER WITH AN ADDITIONAL TRANSMISSION ZERO

In general, the cut-off frequency of the low-pass filter can be adjusted by setting proper values of the lumped elements of the
filter [9]. To realize the desired capacitive and inductive values of the filter elements by the stubs of the high/low-impedance transmission lines, the characteristic impedance $Z_0$ and effective dielectric constant $\varepsilon_{\text{eff}}$ of these transmission lines have to be determined. For this, a commercial MoM simulator was used [10]. Now the transmission line lengths $l_{k,j}$ of the filter elements, assumed to be much shorter than the wavelength, can be calculated from

$$C_k = \frac{l_k}{Z_{0k}v_{ph,k}}$$

$$L_j = \frac{l_jZ_{0j}}{v_{ph,j}}$$

where, indices $k$ and $j$ correspond to stubs with capacitive and inductive character, respectively. $C_k$ and $L_j$ are the capacitance and inductance of the stubs. The phase velocity

$$v_{ph} = \frac{c}{\sqrt{\varepsilon_{\text{eff}}}}$$

where $c$ is the speed of light. Having calculated the transmission line lengths, the filter layout can be constructed, but some additional optimization is still needed.

A microstrip low-pass filter, Fig. 4, was designed on both substrate sides by opening apertures in the ground metallization under the high-impedance transmission line [11]. The aim was to increase the inductance of the narrow strips, which leads to a very compact filter of a small size. However, no resonant effect that can introduce transmission zeroes into the filter response was achieved in this structure. Replacing some of the apertures by the proposed interdigital structure introduces transmission zeroes. The number of transmission zeroes is equal to the number of apertures replaced.

One transmission zero is introduced into the filter response by replacing the central aperture by the interdigital slot structure. This slot, however, shifts the cut-off frequency of the filter down. To shift the cut-off frequency back, it is necessary to reduce the inductance of the narrow stripline that is located over the slot. This can easily be done by increasing the width of the strip. Fig. 5 shows the top and bottom layouts of the designed filter. Fig. 6 shows the measured and simulated return and insertion loss of the filter. A transmission zero, which improves behavior of the filter stop band, is observed at 6.32 GHz. This frequency differs slightly from that predicted and marked by point I in Fig. 3 due to the detuning effect of top metal patches. The calculated group delay varies within the pass-band by 0.275 ns.

**IV. LOW-PASS FILTER WITH TWO ADDITIONAL TRANSMISSION ZEROES**

Two interdigital slots with different finger lengths inserted in the positions of the two apertures introduce two transmission zeroes. Fig. 7 shows the top and bottom layouts of the filter.
V. CONCLUSION

An interdigital DGS slot was introduced in this letter and applied for filter design. The main advantages of the proposed DGS structure are its compact size and the fact that the parallel resonant frequency can be controlled without changing the overall slot area of the DGS. Very compact quasi-elliptic low-pass filters using this DGS were designed and fabricated. The transmission zeros introduced to the filter responses improve their stop-band behavior. Good agreement was achieved between the simulated and measured data.

REFERENCES