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Thesis title:	Open Hardware Motion Controller for Model-Based Rapid Prototyping with NuttX RTOS
Author's name:	Štěpán Pressl
Type of thesis :	bachelor
Faculty/Institute:	Faculty of Electrical Engineering (FEE)
Department:	Department of Control Engineering
Thesis reviewer:	Ing. Pavel Píša, Ph.D.
Reviewer's department:	Department of Control Engineering

## **II. EVALUATION OF INDIVIDUAL CRITERIA**

## Assignment

## How demanding was the assigned project?

The design of the complete motion control system hardware is complex task and plan was not to only design single purpose minimal platform but system for broad range of applications from PMSM, DC, stepper motors to special purpose actuators like piezoelectric elements with all kinds of communication interfaces (CAN FD, Ethernet, RS-485). Even that KyR study program is missing proper hardware design related courses as well as proper processor systems knowledge basic introduction, Štěpán Pressl has learned all these skills and with some consultation from Ing. Petr Porazil (Elektroline.cz and PiKRON.com HW expert) has designed system on profession level with open-source KiCAD electronic design/PCB system. The student proved his quality in the frame of the preceding Bachelor Individual Project and that is why initial idea to focus mainly on hardware has been extended to include even integration into pysimCoder rapid control application design system.

## **Fulfilment of assignment**

How well does the thesis fulfil the assigned task? Have the primary goals been achieved? Which assigned tasks have been incompletely covered, and which parts of the thesis are overextended? Justify your answer.

All planned goals have been implemented and six enhancements of SAMv71 peripheral support (incremental encoder index, extension to 32-bits, PWM mutual synchronization and ADC sampling synchronized by PWM) have reached NuttX RTOS mainline source tree as part of its porting to SaMoCon hardware. Nine corresponding design blocks enhancements have reached pysimCoder mainline tree. The student invested even time into adaptation of the system to control the piezoelectric-actuator to allow its use in a research project at Institute of Information Theory and Automation of the Czech Academy of Sciences.

## Activity and independence when creating final thesis

Assess whether the student had a positive approach, whether the time limits were met, whether the conception was regularly consulted and whether the student was well prepared for the consultations. Assess the student's ability to work independently.

The invested time, self-motivation and self-study of the student has been extraordinary. Student even provided time to prepare leaflets and HW for CTU participation at Embedded World Exhibition and Conference in Nuremberg.

## **Technical level**

Is the thesis technically sound? How well did the student employ expertise in his/her field of study? Does the student explain clearly what he/she has done?

The result is professional level hardware prototype design which has reached an serial production HW form factor. There are some small problems caused by gradual understanding the SAMv71 peripherals and limitations which would require the second iteration. But most of these problems have been overcome by manual corrections on PCB for testing on the initial prototype.

## A - excellent.

A - excellent.



challenging



## THESIS SUPERVISOR'S REPORT



## Formal level and language level, scope of thesis

A - excellent.

Are formalisms and notations used properly? Is the thesis organized in a logical way? Is the thesis sufficiently extensive? Is the thesis well-presented? Is the language clear and understandable? Is the English satisfactory?

I consider thesis text as well structured and is provides broad range of important knowledge for continuation on the project and for its users who can use project for lower level (C-language) implemented control systems as well for high level rapid control application design in pysimCoder which allows to utilize graphical model level design approach.

## Selection of sources, citation correctness

A - excellent.

Does the thesis make adequate reference to earlier work on the topic? Was the selection of sources adequate? Is the student's original work clearly distinguished from earlier work in the field? Do the bibliographic citations meet the standards?

32 references to sources as well as 72 pages of the bachelor thesis text body are above average.

## Additional commentary and evaluation (optional)

Comment on the overall quality of the thesis, its novelty and its impact on the field, its strengths and weaknesses, the utility of the solution that is presented, the theoretical/formal level, the student's skillfulness, etc.

The project provides valuable results and Štěpán Pressl plans to continue on the project to investigate NuttX RTOS latencies and cooperate on pysimCoder and NuttX enhancements. The revision of the hardware design is planned in frame of continuing project with Institute of Information Theory and Automation.

# III. OVERALL EVALUATION, QUESTIONS FOR THE PRESENTATION AND DEFENSE OF THE THESIS, SUGGESTED GRADE

Summarize your opinion on the thesis and explain your final grading.

The grade that I award for the thesis is A - excellent.

Date: 3.6.2024

Signature: